

Th

\\ 0.5V
measured at

with a Forward Current of $\leq 30\text{mA}$.

FIGURE 3: WRITE



\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	\ \	\	\ \	\	\ \ \	\	\ \	\	1
Unfr'd Bytes/Sctr *	1010	Set High Order Value *						1

1) to the unsynchronized condition (bit 11=

- - - - -

\ \ \ \\\\	Rotational Speed Tolerance is >=0.5% < 1.0%	\ \ \ \
1 =		11
\ \ \ \\\\	Transfer Rate > 10 MHz <= 15 MHz *3	\ \ \ \
1 =		10
\ \ \ \\\\	Transfer Rate > 5 Mhz <= 10 Mhz *3	\ \ \ \
1 =		9

\\\\\\\\\\\\ 7.7.1.2)

9.3.1.13.

*3 When notched drives are

\\\\\\\\\\\\

9.3

- - - - -

(for Command Modifier 0111)

Data

(for Command M implemented. This byte should be implemented to suppo

> 15 usec

| 1 = Not NRZ

If set to Master Control (7-0 = x'03'), the drive shall generate the Master Sync signal and also respond to the Slave Sync signal received from another Sync source.

A synchronized drive may be set to asynchronous operation (7-0 = x'02').

\\ \ the offset is \\ \\ \\ \\ \\ a drive designated as Master Control or Slave design by the number specified e.g. if set to 64, \\ \\ \\ \\ \\ position behind the Slave rota Sync signal. A drive designated as Master shall reject this command Sync

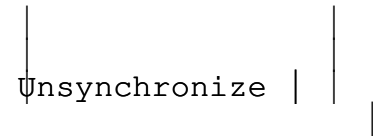
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\\ \\ \\ \\ 0000 0000

\\ \\ \\ \\ 0000 0001

\\ \\ \\ \\ 0000 0011

0000 0010 = Unsynchronize



(see Table 17)

15 usec head switching time shall

(for Command Modifier 0111)

- - - - -

if subscripts are not supported, by \\ by the "Write-to-Read Recovery Time" subscript, or if
See 7.7.1(8) Configuration Data Response. See 7.7.1.

- - - - -

a bit string of
during this

\\ ADR Check Bytes
be zeros.

- - - - -

8.4.4 ISG Speed Tolerance and Format Speed Tolerance Gap.

The ISG is included in the format to allow for all
\\\\\\\\\\\\\\\\\\\\ Tolerance Gap

\\\\\\\\\\\\\\\\\\\\

\\\\\\\\\\\\\\\\\\\\ There may also be a Format Speed Tolerance Gap which extends the IS
\\\\\\\\\\\\\\\\\\\\ the end o

\\\\\\\ filler
 \\\\\\\ end of t
 \\\\\\\ number o
 \\\\\\\ Response r

\\\\\\\ Format
 | | |

\\\\\\\ Request
 Command to

\\ >=

Typically, this bit is

\\ >= If
 and <1.0

>10 MHz,

\\ <=

Modifier 0011)

\\\\\\\ Specific (for Command
 Modifi

\\\\\\\\\\\\ Spindle is Synchronized

\\\\\\\\\\\\ \\ \\ \\ \\ \\ \\ \\ be set to indicate that the drive is always present to of being synchronized is synchronized. Upon a change from the synchronized of b to the unsynchronized condition, the drive shall assert ATTENTION to the

[This has

\\ drive SYNC signal
around by the controller to become SLAVE SYNC, o
designated as Master
to become the SLAVE SYNC (only the SYNC driv
signal is generated). MASTER SYNC
designated as Master master designated as Master at least once per r
be at a higher frequency.

SLAVE SYNC received by a dri

\\

NOTE: A drive Master Control master Master Control does no
its MASTER SYNC signal
\\ \\

In the e

\\ 0000 0000
\\ 0000 0001
\\ 0000 0011
0000 0010 = Unsynchronize | | +----

\\

\\

If a drive is set
SLAV

If a drive is set to Master Control it generates MASTER SYNC an
it as a signal.
\\ \\
\\ SLAVE SYNC to the slaves.

If a drive is set to Unsynchronized it ignores SLAVE SYNC